ORGANIZATION STEERING COMMITTEE:

E. A. Emerson
Oscar H. Ibarra
Insup Lee
Doron A. Peled
Farn Wang
Hsu-Chun Yen

ORGANIZING CHAIR:

Hsu-Chun Yen

PROGRAM CHAIR:

Farn Wang

SPECIAL TRACK CHAIRS:

(1) Teruo Higashino

(2) Pao-Ann Hsiung

(3) Chung-Yang Huang Bow-Yaw Wang

PROGRAM COMMITTEE:

Tommaso Bolognesi (Italy) Tevfik Bultan (USA) Sungdeok Cha (Korea) Yung-Ping Cheng (Taiwan) Jin-Young Choi (Korea) Jing-Song Dong (Singapore) Jifeng He (China) Teruo Higashino (Japan) Pao-Ann Hsiung (Taiwan) Chung-Yang Huang (Taiwan) Oscar H. Ibarra (USA) Insup Lee (USA) Huimin Lin (China) Doron A. Peled (UK) Scott D. Stoller (USA) *Yih-Kuen Tsay* (Taiwan) Bow-Yaw Wang (Taiwan) Farn Wang (Taiwan) *Hsu-Chun Yen* (Taiwan) Tomohiro Yoneda (Japan)

SPONSORS



Call for Papers

ATVA 2004

2nd International Symposium on Automated Technology for Verification and Analysis

National Taiwan University

<u>Sunday 31 October - Wednesday 3 November 2004.</u> http://cc.ee.ntu.edu.tw/~atva04

Encouraged by the success of the first ATVA in December 2003 and the promise of related research and industry in East Asia, we are very happy to announce the second ATVA. The emphasis of ATVA 2004 will continue to be on various mechanical and informative techniques, which can give engineers valuable feedbacks to quickly converge their designs according to the specifications. ATVA 2004 will be a back-to-back occurrence with APLAS 2004 (Asian Symposium on Programming Languages and Systems, 4-6 November 2004) in Taipei.

SCOPE OF INTEREST: The scope of interest includes the following research areas: parametric analysis (parameter synthesis), automated synthesis, optimization, performance analysis, automated tool supports, model-checking theory, theorem-proving theory, state-space reduction techniques, languages in automated verification, real-time systems, embedded systems, infinite-state systems, Petri-nets, UML, synthesis, practice in industry, decidability and complexity issues, case studies. Special emphasis will be on the algorithms, complexities, tools, and experiments of automated verification and analysis.

SPECIAL TRACKS:

ATVA 2004 will also have three special tracks with independent paper reviewing processes. The three tracks are (1) Design of Secure/High-reliable Networks, (2) HW/SW Coverification and Cosynthesis, and (3) Hardware Verification.

KEYNOTE SPEAKERS:

Dr. Robert Kurshan (Cadence, USA)

Prof. Rajeev Alur (U. Pennsylvania, USA)

PAPER SUBMISSION: Each submission is limited to 15 pages with no more than 5000 words. Submissions must be written in English. The formal proceedings will be available at ATVA 2004 and may appear in LNCS, Springer-Verlag, pending approval. An accepted paper without a registered author to ATVA 2004 by the deadline of camera-ready copy will not be included in the formal proceedings. Web-based submission is available at http://cc.ee.ntu.edu.tw/~atva04. Extended versions of selected papers from the conference series will be solicited for publication in special issues of the International Journal of Foundations of Computer Science (IJFCS) (http://www.cs.ucsb.edu/~ijfcs).

IMPORTANT DATES:

20 May 2004, submission deadline

10 July 2004, acceptance notification

20 August 2004, camera-ready copy

31 October-3 November 2004, ATVA 2004